

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 07-302175

(43)Date of publication of application : 14.11.1995

(51)Int.Cl.

G06F 3/08
G06F 3/06
G06F 12/16
G11C 16/06

(21)Application number : 06-095125

(71)Applicant : TOSHIBA CORP

(22)Date of filing : 09.05.1994

(72)Inventor : SUKEGAWA HIROSHI

MAKI YASUNORI

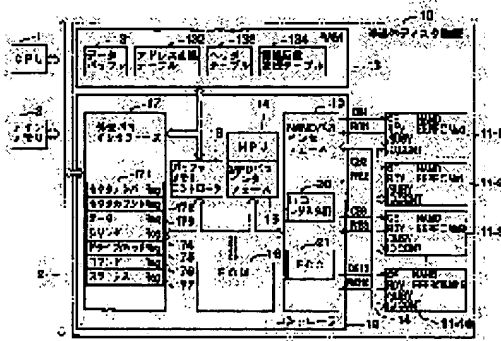
INAGAKI TAKASHI

(54) SEMICONDUCTOR DISK DEVICE

(57)Abstract:

PURPOSE: To improve the reliability of storage data in a flush EEPROM.

CONSTITUTION: An EEC arithmetic circuit 21 calculates data strings transferred from a data buffer 131 in the unit of 256 bytes from a processor 14 to generate an ECC corresponding to the data string. The generated ECC is added to the data string in 256 bytes and transferred to a data register of flush EEPROMs 11-1-11-16. Thus, even when a defective cell of the same bit location of plural pages of the flush EEPROMs takes place, the defective cell including the data string being an object of ECC operation is only one. Thus, error check and correction are implemented by the usual simple ECC arithmetic operation without using a complicated ECC arithmetic equation having a high data recovery capability and then the semiconductor disk device 10 with high storage reliability and capable of high speed access is realized.



LEGAL STATUS

[Date of request for examination]